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10/26/01*

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:  
**MANFRED HORSTMANN  
ROLF STEPHAN  
KARSTEN WIECZOREK  
STEPHEN KRUEGEL**

Examiner: Unknown

Group Art Unit: 2812

Att'y Docket: 2000.064200/DE0053

Serial No.: 09/847,622

Filed: May 2, 2001

For: **FIELD EFFECT TRANSISTOR WITH  
REDUCED GATE DELAY AND  
METHOD OF FABRICATING THE  
SAME**

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**PRELIMINARY AMENDMENT**

**CERTIFICATE OF TRANSMISSION**

I hereby certify that this correspondence is being facsimile transmitted to the Patents and Trademark Office, Washington D.C., 20231, on the date below.

October 11, 2001

*May Paul*  
SIGNATURE

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Please substitute this Preliminary Amendment for the Preliminary Amendment mailed on October 1, 2001. Please amend the above-referenced patent application as follows:

**IN THE CLAIMS:**

Please amend claim 1 as follows:

1. (Amended) A transistor, comprising:

a substrate;

an active region defined in said substrate;

a gate insulation layer formed above said active region; and

*Sub  
B1 > A1*

11/21/2001 AFORD1 00000002 500786 09847622

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T8

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<b>No. of Pages:</b>	6	<b>Date:</b>	October 11, 2001
<b>Re:</b>	S/N 09/847,622	<b>File:</b>	2000.064200

Urgent    For Review    For Your File    Please Reply    Please Handle

\* w/o coversheet

ORIGINAL: \_\_\_ Will follow     X   Will not follow

• **Comments:**

Per our telephone conversation, attached is the revised Preliminary Amendment.

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